

ABSTRACT

A memory system has a circuit board provided with a first slot
5 connector into which a first memory module is inserted. A sec-
ond slot connector is provided into which a second memory mod-
ule is inserted. The first and the second memory modules are
connected via a flexible bridge. The flexible bridge extends
from respective ends of the memory modules opposite to that
10 ends thereof which are inserted into the connector slots. The
flexible bridge provides a signal bus between the memory mod-
ules.

Fig. 1